CH920020037USI (WYC) (8738-668)

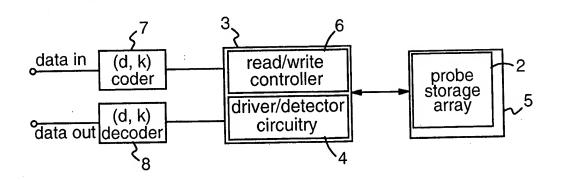
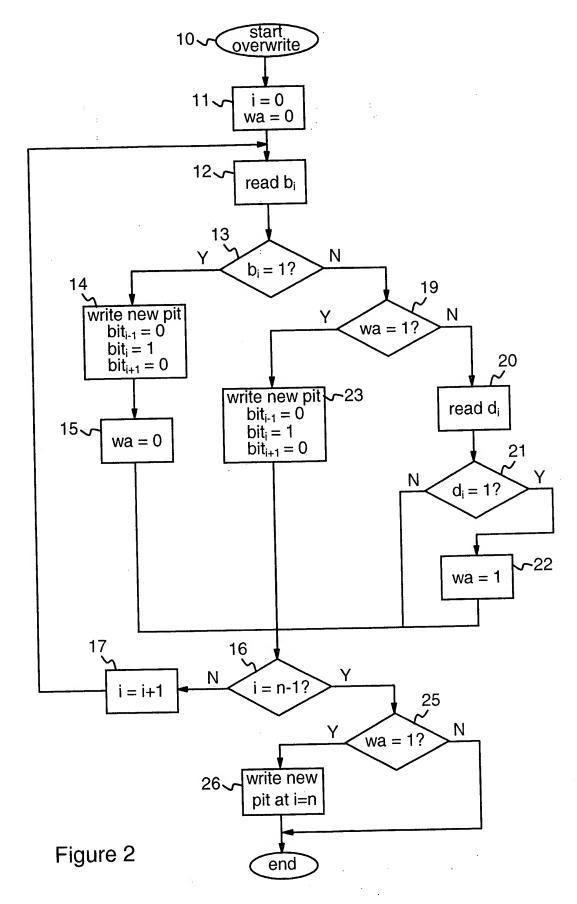


Figure 1

						4 ····		writ (w	e a /a =		S					S	pare bit
	:				;								← ►				†
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
•	1	0	1	0	0	1	0	0	0	1	0	1	0	0	1	0	
coded data b _i :	0	0	0	0	1	0	1	0	0	1	0	0	1	0	0	0	
reads:	R	-	-	-	-	R	-	R	R	-	R	R	-	R	R	-	
writes:	-	W	W	W	W	-	W	-	-	W	-	-	W	-	-	W	w
													•	•		•	:
write results																	
i = 1:	0	1	0	0	0	1	0	0	0	1	0	1	0	0	1	0	
i = 2:	0	0	1	0	0	1	0	0	0	1	0	1	0	0	1	0	
i = 3:	0	0	0	1	0	1	0	0	0	1	0	1	0	0	1	0	
i = 4:	0	0	0	0	1	0	0	0	0	1	0	1	0	0	1	0	
i = 6:	0	0	0	0	1	0	1	0	0	1	0	1	0	0	1	0	
i = 9:	0	0	0	0	1	0	1	0	0	1	0	1	0	0	1	0	
i = 12:	0	0	0	0	1	0	1	0	0	1	0	0	1	0	1	0	
				0	1	0	1	0	0	1	0	0	1	0	0	1	
i = 15:	0	0	0		ا	_		_			_			0		0	-1
i = 16:	0	0	0	0	1	0	1	0	0	1	0	0	1	U	. 0	U	•

Figure 3

219 CH920020027US1(8728-468)

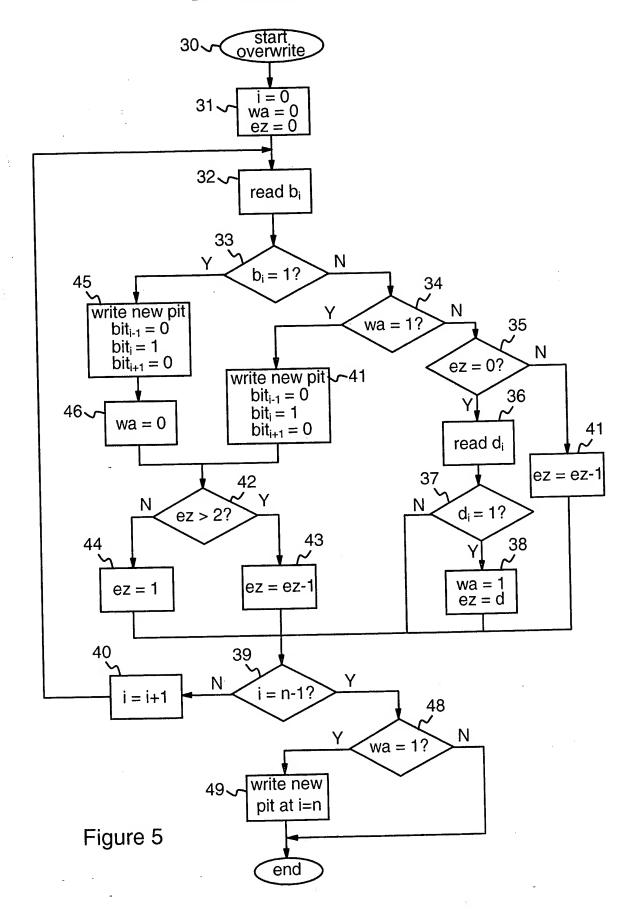


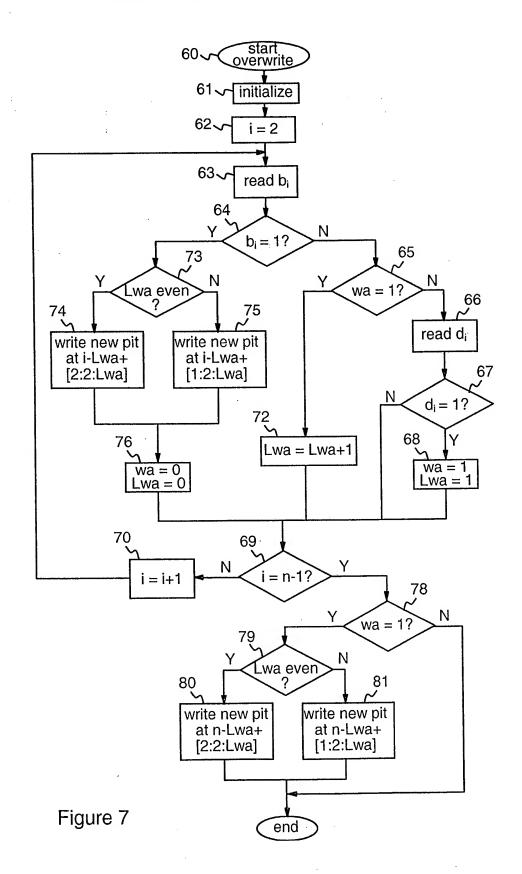
		<u></u>				√	 	→ V		are a =				: ;		. S	bit	re
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
old data d _i :	1	0	1	0	0	1	0	0	0	1	0	1	0	0	1	0		
coded data b _i :	0	0	0	0	1	0	1	0	0	1	0	0	1	0	0	0		
reads:	R	-	-	-	-	-	-	-	R	-	-	R	-	-	R	-		
writes:	-	W	W	W	W	-	W		-	W	-	-	W	-	-	W	W	

Figure 4

		•				•		- ∨		are a =						<u> </u>	spare bit
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
old data d _i :							0									0	
coded data b _i :	0	0	0	0	1	0	1	0	0	1	0	0	1	0	0	0	
reads:	R	-	-	-	-	-	-	-	-	-	-	-	-	-	R	-	
writes:	-	W	W	W	W	-	W	-	-	W	-	-	W	-	-	W	W

Figure 6





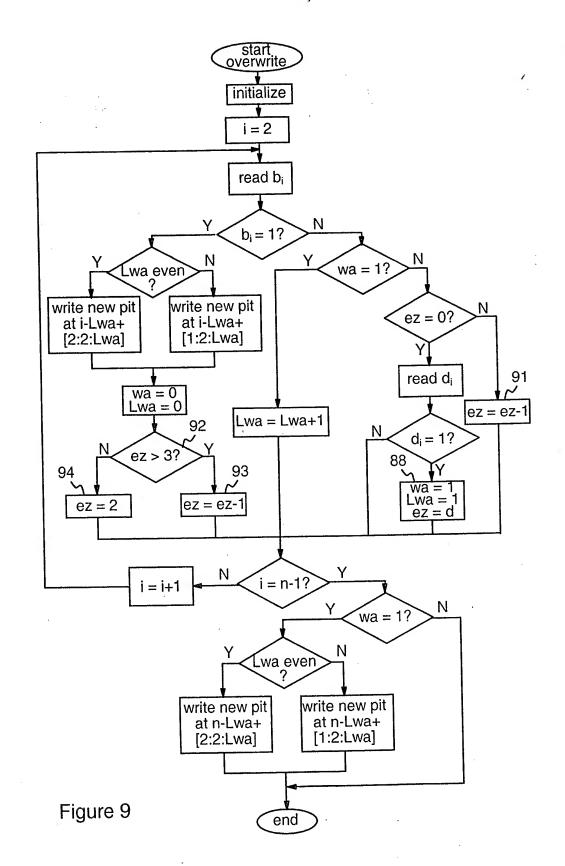
									te a va =	rea: : 1)	S					s	pare bit
	:	-				 ;			: ;				:			:	↓
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
old data d _i :	1	0	0	1	0	0	0	0	1	0	0	0	1	0	0	-1	
coded data b _i :	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	0	
reads:	R	-	-	-	-	-	R	R	R	-	R	R	-	R	Ŗ	R	
writes:	-	W	-	W	-	W	-	-	-	w	-	-	W	-	-	-	W
write results		•								•							
i = 1:	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0	1	
i = 3:	0	0	0	1	0	0	0	0	1	0	0	0	1	0	0	1	
i = 5:	0	0	0	0	0	1	0	0	1	0	0	0	1	0	0	1	
i = 9:	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	1	
i = 12:	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	1	
i = 16:	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	0	1

Figure 8

			write areas (wa = 1)									S	pare bit 1				
		-		·													V
bit positions i:	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
old data d _i :	1	0	0	1	0	0	0	0	1	0	0	0	1	0	0	1	
coded data b _i :	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	0	
reads:	R	-	-	-	-	-	-	-	-	-	-	-	-	7	-	R	
writes:	-	W	-	W	-	W	-	-	-	W	-	-	W	-	-	-	W

Figure 10

14. 40.



Code:	(1, 3)	(2, 10) code 1	(2, 10) code 2
, N _w	0.41	0.38	0.37
N _r	0.22	0.42	0.41
N _{method2}	0.44	0.61	0.57
N _{code}	0.37	0.2	0.22

Figure 11

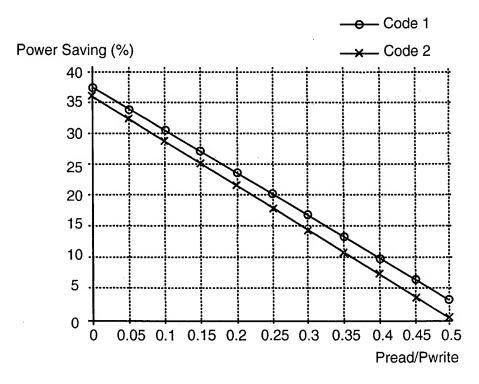


Figure 12

Code:	(2, 10) code 1	(2, 10) code 2
N _{w2}	0.24	0.25
N _{r2}	0.29	0.26
N _{method3}	0.3	0.29
N _{code}	0.2	0.22

Figure 13

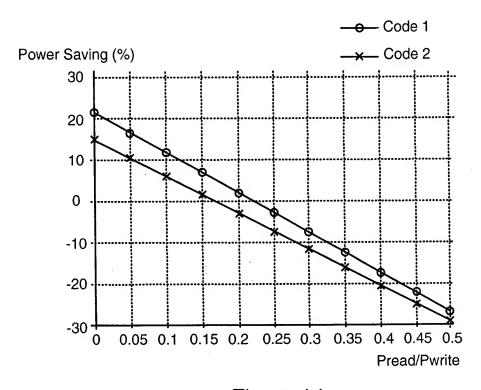


Figure 14